

SPECIFICATION AMENDMENTS

Please amend the specification by replacing the paragraphs below.

[0026] United States Patent Applications, Serial Nos. 10/254483 and 10/254290 filed on September 24, 2002, published as 2004-0060031-A1 and 2004-0057285-A1, disclose a memory architecture in which a page of contiguous memory storage units are programmed or read in parallel. As programming is performed on a page of contiguous memory storage units, during the process those memory storage units that have been programmed to their targeted state will be program-inhibited or locked out from further programming. In a preferred scheme, the memory storage units are locked out by floating their channels and boosting the voltage there to inhibit programming. This boosted voltage creates a significant perturbation on an adjacent storage unit still under programming.

[0052] The sense module 380 shown in **FIG. 4A, FIG. 4B and FIG. 12** is preferably implemented in a memory architecture configured to perform all-bit-line sensing. In other words, contiguous memory cells in a row are each connectable to a sense module to perform sensing in parallel. Such a memory architecture is also disclosed in co-pending and commonly assigned United States Patent Application Serial No. 10/254,483 filed by Cernea et al., on September 24, 2002 entitled, "Highly Compact Non-Volatile Memory And Method ~~Thereof~~." Thereof", which published as 2004-0060031-A1. The entire disclosure of said patent application is hereby incorporated herein by reference.

[0112] **FIGs. 8(H)-8(O)** illustrates the timing of the preferred sense module shown in **FIG. 12** in relation to the features of the present invention. Detailed description of the operation of the preferred sense module in regards to other inventive features has been described and claimed in co-pending and co-owned United States Patent Application Serial Number, 10/254830 filed on September 24, 2002 by Adrian-Raul Cernea and Yan Li, published as 2004-0057287-A1. The entire disclosure of the referenced application is hereby incorporated herein by reference.